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03/15/02
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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

| APPL NUM | FILING DATE | CLASS | SUBCLASS | GAU | EXAMINER |
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| 10099641 | 03/15/2002 | 438 | 75 | 2829 | |

**APPLICANTS: Catabay Wilbur; Schinella Richard;

**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A DIV OF 09/426,056 10/22/1999 PAT 6,391,795

** FOREIGN APPLICATIONS VERIFIED:

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|---|---|--|---------------------------------|
| PG-PUB | DO NOT PUBLISH <input type="checkbox"/> | RESCIND <input type="checkbox"/> | |
| Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiner's initials | | <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no | ATTORNEY DOCKET NO 99-102/1D |
| TITLE : Low k dielectric composite layer for integrated circuit structure which provides void-free low k dielectric material between metal lines while mitigating via poisoning | | | |
| U.S. DEPT. OF COMM./PAT & TM-PTO-436L (Rev. 12-94) | | | |

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|--|-----------|--------------------|----------------|----------------------|
| NOTICE OF ALLOWANCE MAILED | | Assistant Examiner | CLAIMS ALLOWED | |
| Amount Due | Date Paid | | Total Claims | Print Claim for O.G |
| ISSUE FEE | | DRAWING | | |
| | | Sheets Draw. | Fig. Draw. | Print Fig. |
| TERMINAL DISCLAIMER | | Primary Examiner | | Application Examiner |
| PREPARED FOR ISSUE | | | | |
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